IN THE SPECIFICATION

1.   Please amend the first paragraph on column 3, from line 1 through line 6, to read as

follows:

Therefore, according to the present invention, the tilt of the image of the screen is corrected

in the normal manner in the on-state mode. On the other hand, in the cases of the standby mode, the

suspend mode or the power-off mode, the tilt correcting coil does not consume any power, thereby

satisfying the power consumption definition of the power-off mode.

2.   Please amend the two consecutive paragraphs on column 3, from line 12 through line

17, to read as follows:

FIG. 1 and 1A illustrate an embodiment of a circuit to which the method for controlling the

power consumption according to the present invention is applied; and

FIG. 2 is a signal flow chart showing the operation of the microcomputer of FIGS. 1 and

1A, which is used for controlling the power consumption according to the present invention.

3.   Please amend the paragraph on column 3, from line 20 through 34, to read as

follows:

Referring to FIGS. 1 and 1A, circuitry 60 including tilt correcting signal circuitry for

controlling the power consumption comprises: a microcomputer 20 for controlling the DPMS operations of a monitor in accordance with the presence and absence of horizontal and vertical synchronizing signals input from a computer system, and for setting a tilt correcting value for the images of the screen in accordance with key signals of a keyboard or keyboard section 10 and outputting tilt correcting pulse width modulated (PWM) signals; an integrator 30 for converting the tilt correcting PWM signals output from microcomputer 20 to dc voltages; and a tilt correcting signal outputting section 40 for amplifying the output voltages of integrator 30 to output tilt correcting signals. A tilt correcting coil 50 corrects the tilt of the images of the screen in accordance with the output voltages of tilt correcting signal outputting section 40.

4.   Please amend the first paragraph on column 4, from line 1 through line 7, to read as follows:

At a step S1, once the computer system is initially started or after a resetting operation, microcomputer 20 receives horizontal and vertical synchronizing or synchronization signals from the computer system in a normal on-state mode. At steps S2-S4, the microcomputer 20 determines whether horizontal and vertical synchronizing signals are being input from the computer system.